



Date: December 12, 2003

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(REV. 05/03) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>	ATTY. DOCKET NO. TI-34685 (UNITI-169XX)	APPLICATION NO. 10/662,596
	APPLICANT: James B. Cho, et al.	
	FILING DATE 9/15/03	TC ART UNIT 2816

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	PUBLICATION/ ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE
DL	US5,859,606	1/12/99	Schrader, et al.	341	144	—
DL	US6,335,644 B1	1/1/02	Carbone	327	141	—
DL	US6,356,122 B2	3/12/02	Sevalia, et al.	327	105	—
DL	US6,583,655 B2	6/24/03	Takahashi, et al.	327	160	—
	US					
	US					
	US					

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS (including Author, Title, Date, Pertinent Pages, etc.)

DL	ECE1352 Analog Integrated Circuits Reading Assignment: Phase Interpolating Circuits, Kostas Pagiamtzis, November 12, 2001, pp. 1-19.

EXAMINER [Signature]	DATE CONSIDERED 5/1/06
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*EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.